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## APPLICANTS

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|---------------------------------|------------------------------------------------------------------------------------------------------------------|----------------------|-------------------|-----------------|-----------------------|
| 35 USC 119 (a-d) conditions met | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance | TAIWAN               | 3                 | 24              | 3                     |
| Verified and Acknowledged       | <i>K-CL</i>                                                                                                      | Examiner's Signature | Initials          |                 |                       |

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## TITLE

Process integration of SOI FETs with active layer spacer

|                 |                                                                                                                   |                                                                                                                                                                                                                                              |
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| FILING FEE      | FEES: Authority has been given in Paper<br>No. _____ to charge/credit DEPOSIT ACCOUNT<br>No. _____ for following: | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )<br><input type="checkbox"/> 1.18 Fees ( Issue )<br><input type="checkbox"/> Other _____ |
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